

ABSTRACT

Bit value transfer operation instructions are provided. The bit value transfer operation instructions themselves include four instructions, each for selecting a bit value contained in a source bit position of a data memory location and writes the bit value to a destination bit position of another data memory location. Moreover, the instructions specify a source bit position of a data memory location containing a bit value to select, a destination bit position of another data memory location to write the bit value, and the data memory location of an operand from which to read or write the bit value. Processing a bit value transfer operation instruction includes fetching and decoding a bit value transfer instruction. The method further includes executing the bit value transfer instruction on a source bit position of a first data memory location to select a bit value in the source bit position of the first data memory location. The bit position of the first data memory location is specified in the bit value transfer instruction. The method further includes writing the value to a destination bit position of a second data memory location. The destination bit position specified in the bit value transfer instruction.